

IN THE CLAIMS

Please cancel claims 37, 40 and accept amended claims 35, 39 and new claims 42-44:

1-25. (Cancelled)

26. (Previously Presented) A liquid crystal display (LCD), comprising:

a first insulating substrate;

a black matrix formed on the first substrate, wherein the black matrix is mesh-shaped with opening portions exposing pixel areas and disconnected at portions thereof;

an insulating layer formed on and covering both the first substrate and the black matrix,

a gate line assembly formed on the insulating layer, the gate line assembly comprising gate lines proceeding in a horizontal direction, and gate electrodes connected to the gate lines;

a conductive pattern separated from gate lines and covering a gap between the disconnected portions of the black matrix;

a gate insulating pattern formed on and covering both the insulating layer and the gate line assembly;

a semiconductor pattern formed on the gate insulating pattern;

an ohmic contact layer formed on the semiconductor pattern;

a data line assembly formed on the ohmic contact layer, the data line assembly comprising a source electrode and a drain electrode separated from each other, and data lines connected to the source electrode while crossing over the gate lines to define the pixel areas; and

a protective layer covering the data line assembly and the gate line assembly while exposing the gate insulating pattern, the semiconductor pattern, and portions of the insulating layer placed at the pixel areas.

27. (Previously Presented) The LCD of claim 26, further comprising a pixel electrode connected to the drain electrode, the connection of the pixel electrode to the drain electrode being made through the first contact hole.

28. (Cancelled)

29. (Previously Presented) The LCD of claim 27, wherein the black matrix comprises first portions overlapped with the gate lines and the second portions overlapped with the data lines.

30. (Previously Presented) The LCD of claim 27, wherein each pixel electrode has a peripheral portion overlapping the black matrix.

31. (Previously Presented) A method for fabricating a thin film transistor substrate for a liquid crystal display, comprising the steps of:

forming a black matrix on an insulating substrate, wherein the black matrix is mesh-shaped with opening portions exposing pixel areas and disconnected at portions thereof;

forming an insulating layer on the substrate such that the insulating layer covers the black matrix;

forming a gate line assembly on the insulating layer, the gate line assembly comprising gate lines proceeding in a horizontal direction, and gate electrodes connected to the gate lines, wherein the gate lines covering a gap between the disconnected portions of the black matrix;

depositing a gate insulating layer onto the insulating layer;

depositing a semiconductor layer onto the gate insulating layer;

forming an ohmic contact layer on the semiconductor layer;

forming a data line assembly on the ohmic contact layer, the data line assembly comprising a source electrode and a drain electrode separated from each other, and data lines connected to the source electrode while crossing over the gate lines to define the pixel areas, wherein the data lines are narrower than the black matrix;

depositing a protective layer onto the substrate such that the protective layer covers the data line assembly and the gate line assembly; and

forming opening portions exposing the insulating layer at the pixel areas through patterning the protective layer, the gate insulating layer and the semiconductor layer.

32. (Original) The method of claim 31, further comprising the step of forming pixel electrodes such that the pixel electrodes are connected to the drain electrodes,

wherein the step of forming the opening portions comprising a step of forming first contact holes exposing the drain electrodes being formed at the protective layer, and

the pixel electrodes are connected to the drain electrodes through the first contact holes.

33. (Cancelled)

34. (Previously Presented) The method of claim 32, wherein each pixel electrode has a peripheral portion overlapping the black matrix.

35. (Currently Amended) A liquid crystal display (LCD), comprising:
a substrate;
a black matrix formed on the substrate and disconnected at portions thereof;
a gate line formed on the substrate;
a data line intersecting the gate line;
a pixel electrode formed on the substrate; and
a conductive pattern formed on the substrate, separated from the gate line and covering a gap between the disconnected portions of the black matrix,
wherein the black matrix is mesh-shaped.

36. (Previously Presented) The LCD of claim 35, wherein the gate line is formed on the black matrix.

37. (Canceled)

38. (Previously Presented) The LCD of claim 35, wherein the gate line and the conductive pattern are formed on the same plane.

39. (Currently amended) A method for manufacturing a liquid crystal display, comprising steps of:

forming a black matrix layer on a substrate, the black matrix being disconnected at portions thereof;

forming a gate line on the black matrix layer;

forming a data line intersecting the gate line;

forming a pixel electrode on the substrate; and

forming a conductive pattern on the substrate, the conductive pattern being separated from the gate line and covering a gap between the disconnected portions of the black matrix layer,

wherein the steps of forming the gate line and the conductive pattern comprise steps of:

forming a conductive layer; and

patterning the conductive layer to simultaneously form the gate line and the conductive pattern.

40-41. (Cancelled)

42. (New) A liquid crystal display (LCD), comprising:
- a substrate;
 - a black matrix formed on the substrate and disconnected at portions thereof;
 - a gate line formed on the substrate;
 - a data line intersecting the gate line;
 - a pixel electrode formed on the substrate; and
 - a conductive pattern formed on the substrate, separated from the gate line and covering a gap between the disconnected portions of the black matrix,
- wherein the gate line and the conductive pattern are formed on the same plane.
43. (New) The LCD of claim 42, wherein the gate line is formed on the black matrix.
44. (New) The LCD of claim 42, wherein the black matrix is mesh shaped.